Harvard School of Engineering and Applied Sciences - CS 152: Programming Languages

Large-step operational semantics for IMP; Properties of IMP

Lecture 4

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1 Large-step operational semantics for IMP

We define large-step evaluation relations for arithmetic expressions, boolean expressions, and commands. The relation for arithmetic expressions relates an arithmetic expression and store to the integer value that the expression evaluates to. For boolean expressions, the final value is in **Bool** = {**true**, **false**}. For commands, the final value is a store.

$$\begin{split} & \Downarrow_{Aexp} \subseteq Aexp \times Store \times Int \\ & \Downarrow_{Bexp} \subseteq Bexp \times Store \times Bool \\ & \Downarrow_{Com} \subseteq Com \times Store \times Store \end{split}$$

Again, we overload the symbol \Downarrow and use it for any of these three relations; which relation is intended will be clear from context. We also use infix notation, for example writing $\langle c, \sigma \rangle \Downarrow \sigma'$ if $(c, \sigma, \sigma') \in \Downarrow_{Com}$. Arithmetic expressions.

$$\frac{\overline{\langle e_1, \sigma \rangle \Downarrow n}}{\langle e_1, \sigma \rangle \Downarrow n} \qquad \qquad \frac{\overline{\langle e_2, \sigma \rangle \Downarrow n_2}}{\langle e_1 + e_2, \sigma \rangle \Downarrow n} \text{ where } n = n_1 + n_2 \qquad \frac{\overline{\langle e_1, \sigma \rangle \Downarrow n_1} \quad \langle e_2, \sigma \rangle \Downarrow n_2}{\langle e_1 \times e_2, \sigma \rangle \Downarrow n} \text{ where } n = n_1 \times n_2$$

Boolean expressions.

 $\langle \mathsf{true}, \sigma \rangle \Downarrow \mathsf{true}$

 $\langle false, \sigma \rangle \Downarrow false$

$$\frac{\langle a_1, \sigma \rangle \Downarrow n_1 \quad \langle a_2, \sigma \rangle \Downarrow n_2}{\langle a_1 < a_2, \sigma \rangle \Downarrow \text{ true }} \text{ where } n_1 < n_2 \qquad \frac{\langle a_1, \sigma \rangle \Downarrow n_1 \quad \langle a_2, \sigma \rangle \Downarrow n_2}{\langle a_1 < a_2, \sigma \rangle \Downarrow \text{ false }} \text{ where } n_1 \ge n_2$$

Commands.

$$\mathsf{SKIP} \underbrace{\langle \mathsf{skip}, \sigma \rangle \Downarrow \sigma}_{\langle \mathsf{skip}, \sigma \rangle \Downarrow \sigma} \qquad \mathsf{ASG} \underbrace{\frac{\langle e, \sigma \rangle \Downarrow n}{\langle x := e, \sigma \rangle \Downarrow \sigma[x \mapsto n]}}_{\langle x := e, \sigma \rangle \Downarrow \sigma[x \mapsto n]} \qquad \mathsf{SEQ} \underbrace{\frac{\langle c_1, \sigma \rangle \Downarrow \sigma'}{\langle c_1; c_2, \sigma \rangle \Downarrow \sigma''}}_{\langle c_1; c_2, \sigma \rangle \Downarrow \sigma''}$$

$$\operatorname{IF-T} \frac{\langle b, \sigma \rangle \Downarrow \operatorname{true}}{\langle \operatorname{if} b \operatorname{ then} c_1 \operatorname{ else} c_2, \sigma \rangle \Downarrow \sigma'} \qquad \qquad \operatorname{IF-F} \frac{\langle b, \sigma \rangle \Downarrow \operatorname{false}}{\langle \operatorname{if} b \operatorname{ then} c_1 \operatorname{ else} c_2, \sigma \rangle \Downarrow \sigma'}$$

$$\begin{array}{c} \text{WHILE-F} & \underbrace{\langle b, \sigma \rangle \Downarrow \text{ false}}_{\langle \text{while } b \text{ do } c, \sigma \rangle \Downarrow \sigma} & \text{WHILE-T} & \underbrace{\langle b, \sigma \rangle \Downarrow \text{ true }}_{\langle \text{while } b \text{ do } c, \sigma \rangle \Downarrow \sigma'} & \underbrace{\langle \text{while } b \text{ do } c, \sigma' \rangle \Downarrow \sigma''}_{\langle \text{while } b \text{ do } c, \sigma \rangle \Downarrow \sigma''} \end{array}$$

It's interesting to see that the rule for while loops does not rely on using an if command (as we needed in the case of small-step semantics). Why does this rule work?

1.1 Command equivalence

The small-step operational semantics suggest that the loop **while** b **do** c should be equivalent to the command **if** b **then** (c; **while** b **do** c) **else skip**. Can we show that this indeed the case that the language is defined using the above large-step evaluation?

First, we need to be more precise about what "equivalent commands" mean. Our formal model allows us to define this concept using large-step evaluations as follows. (One can write a similar definition using \rightarrow * in small-step semantics.)

Definition (Equivalence of commands). Two commands c and c' are equivalent (written $c \sim c'$) if, for any stores σ and σ' , we have

$$\langle c, \sigma \rangle \Downarrow \sigma' \iff \langle c', \sigma \rangle \Downarrow \sigma'.$$

We can now state and prove the claim that while b do c and if b then (c; while b do c) else skip are equivalent.

Theorem. For all $b \in Bexp$ and $c \in Com$ we have

while *b* do $c \sim$ if *b* then (*c*; while *b* do *c*) else skip.

Proof. Let *W* be an abbreviation for **while** *b* **do** *c*. We want to show that for all stores σ , σ' , we have:

 $\langle W, \sigma \rangle \Downarrow \sigma'$ if and only if **if** *b* **then** (*c*; *W*) **else skip** $\Downarrow \sigma'$

For this, we must show that both directions (\Longrightarrow and \Leftarrow) hold. We'll show only direction \Longrightarrow ; the other is similar.

Assume that σ and σ' are stores such that $\langle W, \sigma \rangle \Downarrow \sigma'$. It means that there is some derivation that proves for this fact. Inspecting the evaluation rules, we see that there are two possible rules whose conclusions match this fact: WHILE-F and WHILE-T. We analyze each of them in turn.

• WHILE-F. The derivation must look like the following.

$$WHILE-F = \frac{ \begin{array}{c} \vdots_1 \\ \hline \langle b, \sigma \rangle \Downarrow \text{ false} \\ \hline \langle W, \sigma \rangle \Downarrow \sigma \end{array}}$$

Here, we use :¹ to refer to the derivation of $(b, \sigma) \Downarrow$ **false**. Note that in this case, $\sigma' = \sigma$.

We can use $:^1$ to derive a proof tree showing that the evaluation of **if** *b* **then** (*c*; *W*) **else skip** yields the same final state σ :

$$IF-F = \frac{ \begin{array}{c} \vdots 1 \\ \hline \langle b, \sigma \rangle \Downarrow \text{ false} \end{array}}{\langle \text{if } b \text{ then } (c; W) \text{ else skip}, \sigma \rangle \Downarrow \sigma}$$

• WHILE-T. In this case, the derivation has the following form.

$$WHILE-T \xrightarrow{\begin{array}{c} \vdots 2 \\ \hline \langle b, \sigma \rangle \Downarrow \text{true} \end{array}} \begin{array}{c} \vdots 3 \\ \hline \langle c, \sigma \rangle \Downarrow \sigma'' \\ \hline \langle W, \sigma' \rangle \Downarrow \sigma' \end{array} \begin{array}{c} \vdots 4 \\ \hline \langle W, \sigma'' \rangle \Downarrow \sigma' \end{array}$$

We can use subderivations \vdots^2 , \vdots^3 , and \vdots^4 to show that the evaluation of **if** *b* **then** (*c*; *W*) **else skip** yields the same final state σ .

IF-T
$$\frac{ \begin{array}{c} \vdots 2 \\ \hline \langle b, \sigma \rangle \Downarrow \text{ true} \end{array}}{\langle \text{if } b \text{ then } (c; W) \text{ else skip}, \sigma \rangle \Downarrow \sigma'} \underbrace{ \begin{array}{c} \vdots 3 \\ \hline \langle W, \sigma'' \rangle \Downarrow \sigma' \\ \hline \langle W, \sigma'' \rangle \Downarrow \sigma' \end{array}}_{\langle c; W, \sigma \rangle \Downarrow \sigma'}$$

Hence, we showed that in each of the two possible cases, the command if *b* then (c; W) else skip evaluates to the same final state as the command *W*.

2 Some properties of IMP

2.1 Equivalence of semantics

The small-step and large-step semantics are equivalent. We state this formally in the following theorem.

Theorem (Equivalence of IMP semantics). For all commands $c \in Com$ and stores $\sigma, \sigma' \in Store$ we have

$$\langle c, \sigma \rangle \longrightarrow^* \langle \mathsf{skip}, \sigma' \rangle \iff \langle c, \sigma \rangle \Downarrow \sigma'.$$

2.2 Non-termination

For a command *c* and initial state σ , the execution of the command may *terminate* with some final store σ' , or it may *diverge* and never yield a final state. For example, the command **while true do** foo := foo + 1 always diverges; the command **while** 0 < i do i := i + 1 will diverge if and only if the value of variable i in the initial state is positive.

If $\langle c, \sigma \rangle$ is a configuration that diverges, then there is no state σ' such that $\langle c, \sigma \rangle \Downarrow \sigma'$ or $\langle c, \sigma \rangle \longrightarrow^* \langle \mathbf{skip}, \sigma' \rangle$. However, in small-step semantics, a diverging computation has an infinite sequence of configurations: $\langle c, \sigma \rangle \longrightarrow \langle c_1, \sigma_1 \rangle \longrightarrow \langle c_2, \sigma_2 \rangle \longrightarrow \ldots$ Small-step semantics can allow us to state, and prove, properties about programs that may diverge. Later in the course, we will specify and prove properties that are of interest in potentially diverging computations.

2.3 Determinism of commands

The semantics of IMP (both small-step and large-step) are *deterministic*. That is, each IMP command c and each initial store σ evaluates to at most one final store. We state this formally for the large-step semantics below.

Theorem. For all commands $c \in Com$ and stores $\sigma, \sigma_1, \sigma_2 \in Store$, if $\langle c, \sigma \rangle \Downarrow \sigma_1$ and $\langle c, \sigma \rangle \Downarrow \sigma_2$ then $\sigma_1 = \sigma_2$.

We need an inductive proof to prove this theorem. However, induction on the structure of command c does not work. (Why? Which of the cases does it fail for?) Instead, we need to perform induction on the derivation of $\langle c, \sigma \rangle \Downarrow \sigma_1$. We first introduce some useful notation.

Let *d* be a derivation. We write $d \Vdash y$ if *d* is a derivation of *y*, that is, if the conclusion of *d* is *y*. For example, if *d* is the following derivation

$\overline{\langle 6,\sigma\rangle \Downarrow 6}$	$\overline{\langle 7,\sigma\rangle \Downarrow 7}$
$\langle 6 \times 7, \sigma \rangle \Downarrow 42$	
$\langle i := 6 \times 7,$	$\sigma \rangle \Downarrow \sigma[i \mapsto 42]$

then we have $d \Vdash \langle i := 42, \sigma \rangle \Downarrow \sigma[i \mapsto 42]$.

We say that derivation *d* has the form $\{d_1, \ldots, d_n\}/y$ if $d \Vdash y$ and the inference rule that *d* used to conclue *y* has premises y_1, \ldots, y_n such that $d_1 \Vdash y_1, \ldots, d_n \Vdash y_n$.

Let *d* and *d'* be derivations. We say that *d'* is an *immediate subderivation* of *d* if *d'* if *d* is of the form $\{d', d_1, \ldots, d_n\}/y$. That is, *d'* is a derivation of one of the premises used in the final rule in the derivation *d*. For example, the derivation

is an immediate subderivation of

$\overline{\langle 6,\sigma\rangle \Downarrow 6}$	$\overline{\langle 7,\sigma\rangle \Downarrow 7}$
$ \qquad \qquad$	
$\langle i := 6 \times 7$	$\langle , \sigma \rangle \Downarrow \sigma[i \mapsto 42]$

Before we commence the proof of the theorem, we will need two lemmas, related to the determinism of the arithmetic and boolean semantics, ψ_{Aexp} and ψ_{Bexp} .

Lemma 1. For all arithmetic expressions $a \in Aexp$, stores $\sigma \in Store$, and integers $n_1, n_2 \in Int$, if $\langle a, \sigma \rangle \Downarrow n_1$ and $\langle a, \sigma \rangle \Downarrow n_2$ then $n_1 = n_2$.

Lemma 2. For all boolean expressions $b \in Aexp$, stores $\sigma \in Store$, and integers $b_1, b_2 \in Bool$, if $\langle b, \sigma \rangle \Downarrow b_1$ and $\langle a, \sigma \rangle \Downarrow b_2$ then $b_1 = b_2$.

These lemmas are straightforward to prove, and can be proved using strucutral induction on arithmetic and boolean expressions respectively.

Proof. We proceed by induction on the derivation of $\langle c, \sigma \rangle \Downarrow \sigma_1$. The inductive hypothesis *P* is

$$P(d) = \forall c \in \mathbf{Com}. \ \forall \sigma, \sigma', \sigma'' \in \mathbf{Store}, \text{ if } \langle c, \sigma \rangle \Downarrow \sigma'' \text{ and } d \Vdash \langle c, \sigma \rangle \Downarrow \sigma' \text{ then } \sigma' = \sigma''$$

Let *d* be a derivation. Assume that the inductive hypothesis holds for all immediate subderivations of *d*. Assume $\langle c, \sigma \rangle \Downarrow \sigma''$ and $d \Vdash \langle c, \sigma \rangle \Downarrow \sigma'$. Since $\langle c, \sigma \rangle \Downarrow \sigma''$, there must be some derivation d_1 such that $d_1 \Vdash \langle c, \sigma \rangle \Downarrow \sigma''$.

We consider the possible cases for the last rule used in derivation *d*.

• Skip

In this case

$$d = \frac{\mathsf{SKIP}}{\langle \mathsf{skip}, \sigma \rangle \Downarrow \sigma},$$

and we have $c = \mathbf{skip}$ and $\sigma' = \sigma$. Since the rule SKIP is the only rule that has the command **skip** in its conclusion, the last rule used in d_1 must also be SKIP, and so we have $\sigma'' = \sigma$ and the result holds.

• ASG

In this case

$$d = \operatorname{ASG} \frac{\vdots}{\langle a, \sigma \rangle \Downarrow n} \frac{}{\langle x := a, \sigma \rangle \Downarrow \sigma'},$$

and we have c = x := a and $\sigma' = \sigma[x \mapsto n]$. The last rule used in d_1 must also be ASG, and so we have $d' \Vdash \langle x := a, \sigma \rangle \Downarrow \sigma[x \mapsto m]$, where $\langle a, \sigma \rangle \Downarrow m$. By the determinism of arithmetic expressions, m = n and so $\sigma'' = \sigma'$ and the result holds.

• Seq

In this case

$$d = \frac{\mathsf{SEQ} - \frac{\vdots}{\langle c_1, \sigma \rangle \Downarrow \sigma_1} - \frac{\vdots}{\langle c_2, \sigma_1 \rangle \Downarrow \sigma'}}{\langle c_1; c_2, \sigma \rangle \Downarrow \sigma'}$$

and we have $c = c_1; c_2$. The last rule used in d_1 must also be SEQ, and so we have

$$d_{1} = \frac{\overset{\vdots}{\langle c_{1}, \sigma \rangle \Downarrow \sigma_{1}'} \quad \overset{\vdots}{\langle c_{2}, \sigma_{1}' \rangle \Downarrow \sigma''}}{\langle c_{1}; c_{2}, \sigma \rangle \Downarrow \sigma''}$$

By the inductive hypothesis applied to the derivation $(c_1, \sigma) \Downarrow \sigma_1$, we have $\sigma_1 = \sigma'_1$. By another \vdots

application of the inductive hypothesis, to the derivation $\overline{\langle c_2, \sigma_1 \rangle \Downarrow \sigma'}$, we have $\sigma' = \sigma''$ and the result holds.

• IF-T

Here we have

$$d = \operatorname{IF-T} \underbrace{\frac{\vdots}{\langle b, \sigma \rangle \Downarrow \operatorname{true}} \quad \underbrace{\frac{\vdots}{\langle c_1, \sigma \rangle \Downarrow \sigma'}}_{\langle \operatorname{if} b \operatorname{ then} c_1 \operatorname{ else} c_2, \sigma \rangle \Downarrow \sigma'}$$

and we have c = if b then c_1 else c_2 . The last rule used in d_1 must be either IF-T or IF-F (since these are the only rules that can be used to derive a conclusion of the form $\langle \text{if } b \text{ then } c_1 \text{ else } c_2, \sigma \rangle \Downarrow \sigma''$). But by the determinism of boolean expressions, we must have $\langle b, \sigma \rangle \Downarrow \text{true}$, and so d_1 must have the following form.

$$d_1 = \overset{\text{IF-T}}{\underbrace{ \begin{matrix} \vdots \\ \langle b, \sigma \rangle \Downarrow \text{true} \end{matrix}}} \underbrace{ \begin{matrix} \vdots \\ \langle c_1, \sigma \rangle \Downarrow \sigma'' \end{matrix}}_{\langle \text{if } b \text{ then } c_1 \text{ else } c_2, \sigma \rangle \Downarrow \sigma''}$$

The result holds by the inductive hypothesis applied to the derivation $\overline{\langle c_1, \sigma \rangle \Downarrow \sigma'}$.

• IF-F

Similar to the case for IF-T.

• While-F

Straightforard, similar to the case for SKIP.

• WHILE-T

Here we have

and we have c = while *b* do c_1 . The last rule used in d_1 must also be WHILE-T (by the determinism of boolean expressions), and so we have

$$d_1 = \overset{\text{HILE-T}}{\underbrace{\langle b, \sigma \rangle \Downarrow \text{true}}} \underbrace{\frac{\vdots}{\langle c_1, \sigma \rangle \Downarrow \sigma'_1}}_{\langle \text{while } b \text{ do } c_1, \sigma \rangle \Downarrow \sigma''}$$

By the inductive hypothesis applied to the derivation $\overline{\langle c_1, \sigma \rangle \Downarrow \sigma_1}$, we have $\sigma_1 = \sigma'_1$. By another

application of the inductive hypothesis, to the derivation $\overline{\langle c, \sigma_1 \rangle \Downarrow \sigma'}$, we have $\sigma' = \sigma''$ and the result holds.

Note: Even though the command c = **while** b **do** c_1 appears in the derivation d of \langle **while** b **do** $c_1, \sigma \rangle \Downarrow \sigma'$, we do not run in to problems, as the induction is over the *derivation*, not over the structure of the command.

In all cases, we have shown that $\forall c \in \mathbf{Com}$. $\forall \sigma, \sigma', \sigma'' \in \mathbf{Store}$, if $\langle c, \sigma \rangle \Downarrow \sigma''$ and $d \Vdash \langle c, \sigma \rangle \Downarrow \sigma'$ then $\sigma' = \sigma''$. Thus we can conclude that P(d) holds for all derivations for the executions of commands. This is equivalent to

$$\forall c \in \mathbf{Com}. \ \forall \sigma, \sigma', \sigma'' \in \mathbf{Store}, \text{if } \langle c, \sigma \rangle \Downarrow \sigma' \text{ and } \langle c, \sigma \rangle \Downarrow \sigma'' \text{ then } \sigma' = \sigma''$$

which proves the theorem.